

# PATENT ABSTRACTS OF JAPAN

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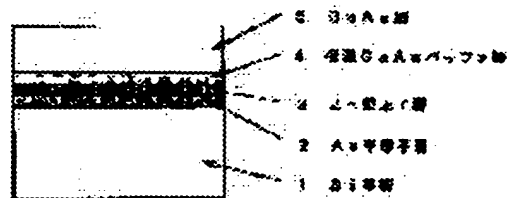
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## (54) HETERO EPITAXIALLY GROWING METHOD

### (57)Abstract:

PURPOSE: To obtain a GaAs epitaxial layer having a low dislocation and high quality by growing a group II element single atomic layer on an As single atomic layer, growing a low temperature GaAs buffer layer thereon and epitaxially growing a GaAs layer on the buffer layer.

CONSTITUTION: An Si (100) board 1 is heated to 250° C, and an As single atomic layer 2 is formed thereon. Then, a single atomic layer 3 of group II element such as Zn, Be, Mg, etc., is formed thereon. Thereafter, while a growing temperature remains at 250° C, a low temperature GaAs buffer layer 4 is grown on the layer 3. Then, the growing temperature is raised to 550° C, and a GaAs epitaxial layer 5 is grown. Thus, a two-dimensional grown can be controlled in an initial step of the GaAs epitaxial growth on the board.



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